Abstracts

A 50-Gbit/s 1:4 demultiplexer IC in InP-based HEMT technology

H. Kano, T. Suzuki, S. Yamaura, Y. Nakasha, K. Sawada, T. Takahashi, K. Makiyama, T. Hirose and Y. Watanabe. "A 50-Gbit/s 1:4 demultiplexer IC in InP-based HEMT technology." 2002 MTT-S International Microwave Symposium Digest 02.1 (2002 Vol. I [MWSYM]): 75-78 vol.1.

We have developed a 50-Gb/s 1:4 demultiplexer (DEMUX) integrated circuit with a wide phase margin of 108 degrees in 0.13-/spl mu/m InP-based HEMT technology. To increase the phase margin, we designed the data and clock distribution with the aim of achieving high symmetry and eliminating multiple reflections. The measured performance of the fabricated 1:4 DEMUX was suitable for practical use in 50-Gbit/s-class applications.

❑ Return to main document.